

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4

Attorney Docket No. 040447-0211

In re patent application of

Osamu OHNISHI

Group Art Unit: 2734

Serial No.: 09/512,384

Examiner: Unassigned

Filed: February 25, 2000

For: APPARATUS FOR DETECTING CORRELATION, SPECTRUM DESPREAD
APPARATUS AND RECEIVER HAVING THE SAME, AND METHOD FOR
DETECTING CORRELATION

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.56 AND 37 C.F.R. § 1.97

Commissioner for Patents
Washington, D.C. 20231

Commissioner:

Submitted herewith on a modified Form PTOSB/08B is a listing of document(s) known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 C.F.R. § 1.56. A copy of the listed document(s) is being submitted to comply with the provisions of 37 C.F.R. §§ 1.97-1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 C.F.R. § 1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* prior art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The instant Information Disclosure Statement is being filed before the mailing date of a first action under the provisions of 37 C.F.R. § 1.97(b)(3). In the alternative, the

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instant Information Disclosure Statement is being filed under the provisions of 37 C.F.R. § 1.97(c)(1).

STATEMENT UNDER 37 C.F.R. § 1.97(e)

The undersigned hereby states in accordance with 37 C.F.R. § 1.97(e)(1) that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of this Statement.

RELEVANCE OF EACH DOCUMENT

The documents listed on the attached PTO/SB/08B were cited as being relevant during the prosecution of the corresponding Japanese application. A copy of an English language abstract of the listed documents, if available, is being provided. The absence of a translation or an English-language counterpart document does not relieve the PTO from its duty to consider this document (37 C.F.R. § 1.98 and M.P.E.P. § 609).

The Examiner in the corresponding Japanese patent application has commented as follows:

Ref # 40447/211
09/512384

SN:

Record (See List of Cited Literature for Cited Literature, etc.)

Claims: 1-13

Cited Literature: 1

Comments:

Cited Literature 1 described a correlation value detection device which outputs a correlation signal indicating

the correlation value between a sample obtained by sampling, at an oversampling rate which is an N-fold (where N is 1 or an integer of 2 or more) chip rate, the range of one symbol period of a signal that was spectrum diffused by a diffusion code with 1 symbol having an M period (where M is an integer of 2 or more) and said diffusion code, this correlation value detection device comprising M memory units storing N samples of input signals, an M-input adder having said input signals or said input signals read from each of said M memory units supplied to each input terminal thereof, and control means writing said input signals into said M memory units in an order by N samples and simultaneously supplying them to M input terminals of said adder, and reading (M-1) times from each of said M memory units said input signals of N samples written in said M memory units and supplying to respective said M input terminals of said adder, wherein the output of said adder is output as the correlation value signal output by the device.

Here, a correlation value detection device detecting a correlation value of a signal spectrum diffused by $L \times M$ period diffusion codes, which comprises an L chip accumulator, a shift register consisting of $L \times (M-1) \times N$ number of D-type flip-flops, an M input adder, and M multipliers is suggested in ARIB. The invention of the present application could easily have been made by a person with ordinary skill in the art based on the description presented in Cited Literature 1 by using the shift register, adder, and multiplier as the configuration components.

List of Cited Literature

Japanese Unexamined Patent Application Publication No.
H10-173485

Record of Results of Prior Art Literature Search

Searched fields IPC 7th Edition H04B 1/69-1/713

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H04J 13/00-13/06

H03H 17/06

Prior Art Literature

Japanese Unexamined Patent Application Publication No.
2000-196498

Japanese Unexamined Patent Application Publication No. H8-
172342

Japanese Unexamined Patent Application Publication No.
2000-252869

Japanese Unexamined Patent Application Publication No.
2000-174661

International patent Application No. 99/6922

Japanese Unexamined Patent Application Publication No.
2000-124836

Japanese Unexamined Patent Application Publication No.
H10-178334

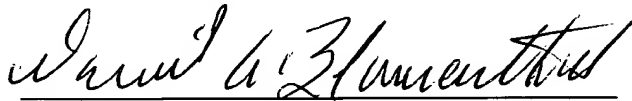
This record of the results of a Prior Art literature search does
not constitute a ground for rejection.

It is believed that the instant information disclosure statement is being filed prior to the mailing date of any Notice of Allowance or Final Office Action. However, if this information disclosure statement is filed after the mailing date of the Notice of Allowance or Final Office Action, then applicant(s) hereby request(s) and authorize(s) the PTO to charge the fee under 37 CFR 1.17(p) to Deposit Account No. 19-0741.

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Applicant respectfully requests that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08B be returned in accordance with M.P.E.P. § 609.

Respectfully submitted,



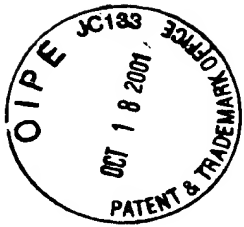
October 9, 2001

Date

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CERTIFICATE OF MAILING

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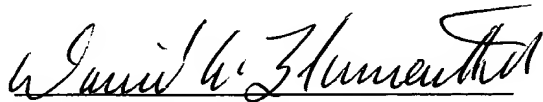
Commissioner:

I hereby certify that the following paper(s) and/or fee along with any attachments referred to or identified as being attached or enclosed are being deposited with the United States Postal Service as First Class Mail under 37 C.F.R. § 1.8(a) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington D.C. 20231.

1. Information Disclosure Statement
2. PTO/SB/08B
3. Postcard

Respectfully submitted,

October 9, 2001
Date


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